





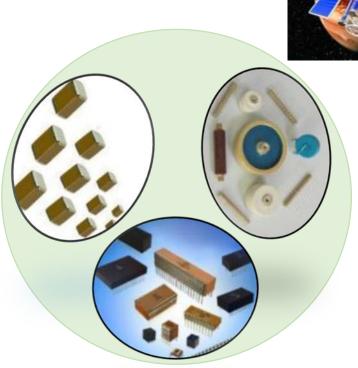
Application of ferroelectric capacitors











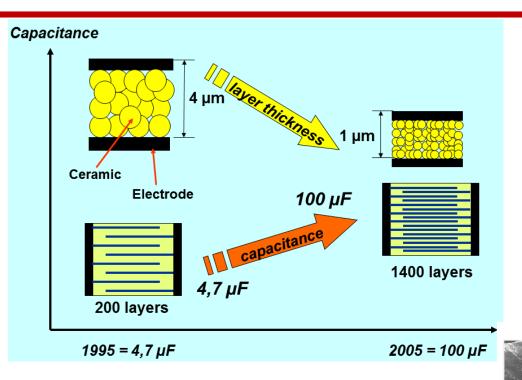


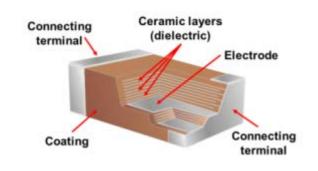






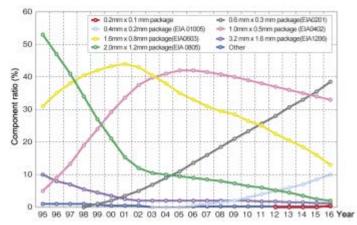
Miniaturization of ferroelectric capacitors

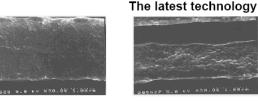


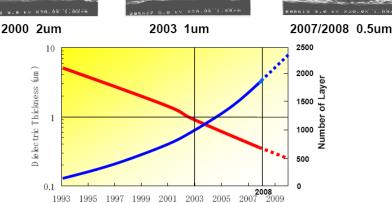


$$C_{MLCC} = C_1 + C_2 + C_3 + C_4 + \dots + C_{n-1}$$

Next generation technology

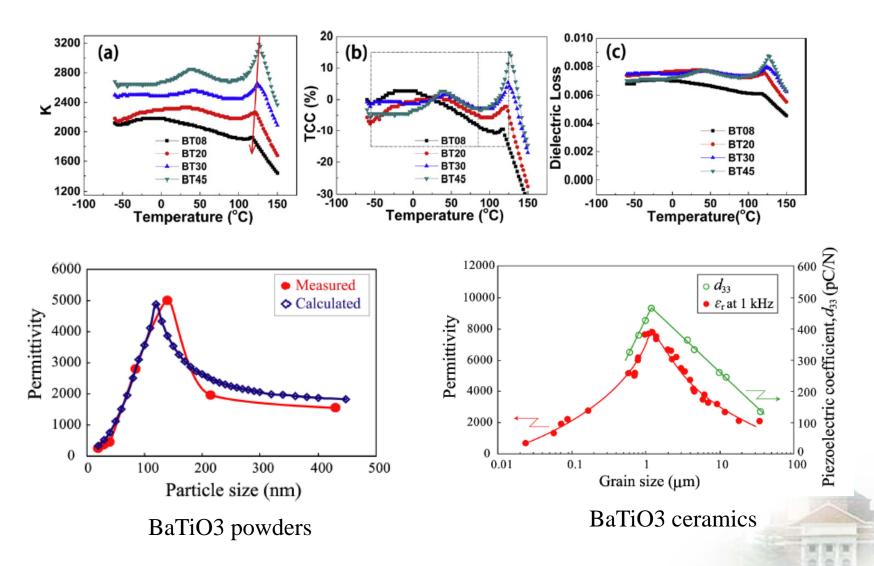






Size effect in ferroelectrics

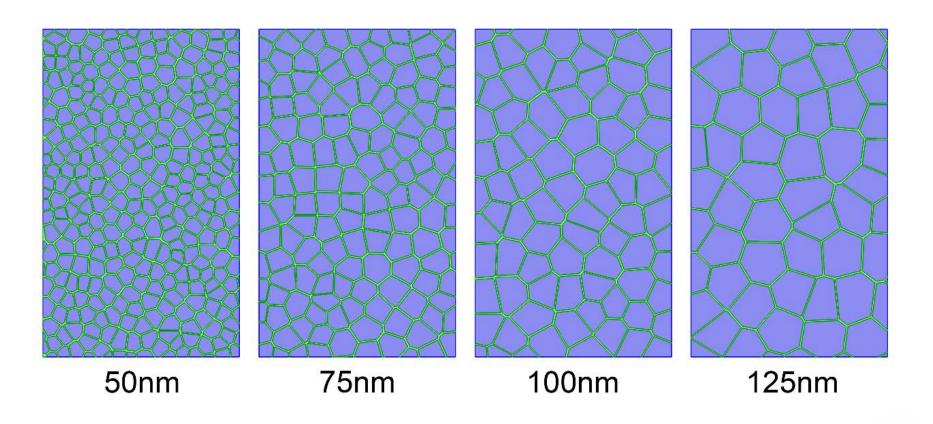




[1]H. Gong, X. Wang, S. Zhang, H. Wen, L. Li, *Journal of the European Ceramic Society* 2014, 34, 1733. [2]T. Hoshina, *Journal of the Ceramic Society of Japan* 2013, 121, 156.

Grains: Voronoi polygonal

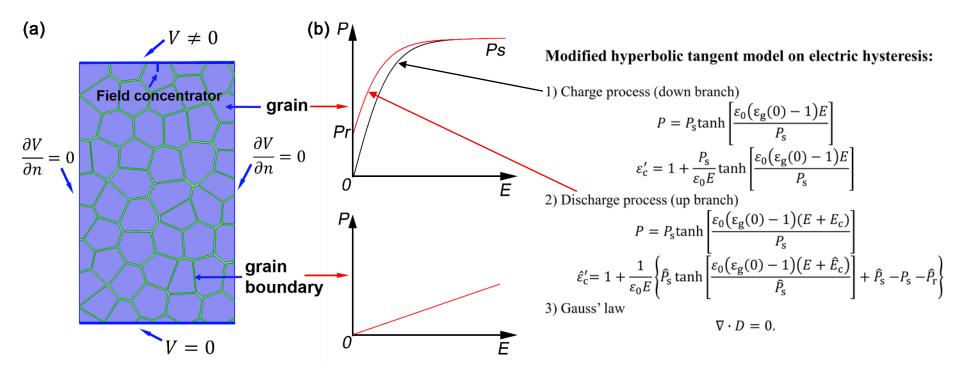






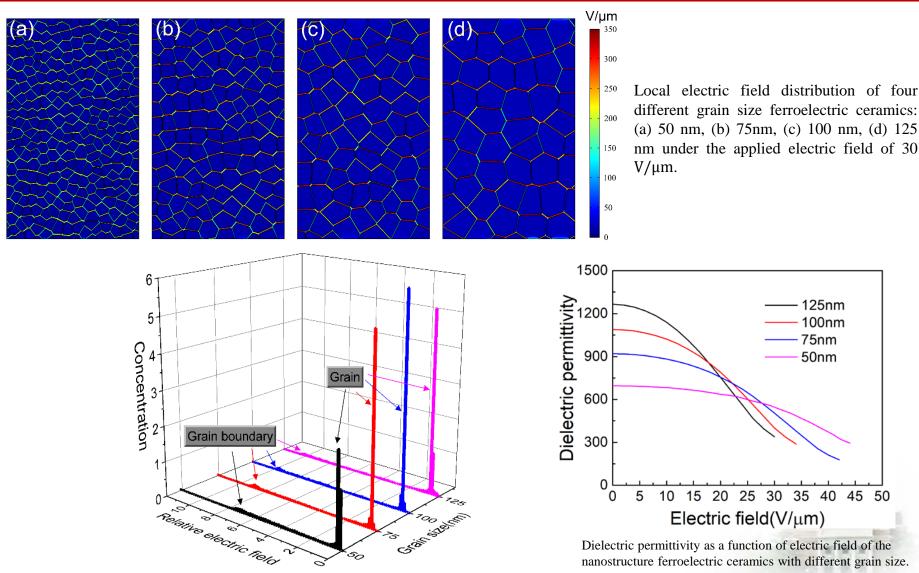
Modeling process





Local electric field distribution





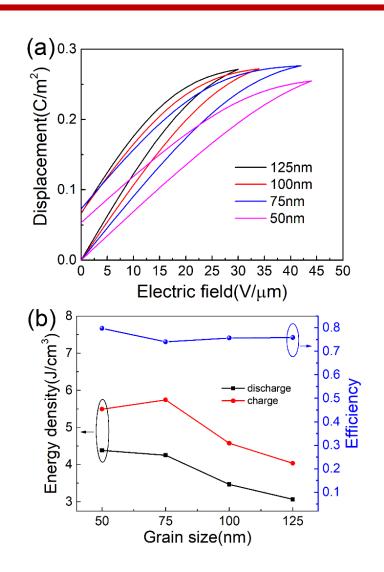
Relative local electric field distribution of these ferroelectric ceramics at various grain size under a given applied electric field of 30 V/ μm .

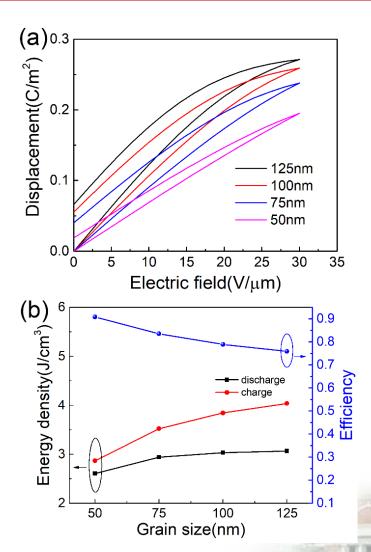
125nm 100nm 75nm 50nm 10 15 20 25 30 35 40 45 50

Dielectric permittivity as a function of electric field of the nanostructure ferroelectric ceramics with different grain size.

D-E loop & Energy density







Conclusion



- The ferroelectric hysteresis loop are numerically calculated through a finite element method based on a classical and modified hyperbolic tangent model.
- ➤ It is found that as the grain size decreases, the dielectric permittivity is reduced.

 Under the same applied electric field, the ferroelectric ceramic with the smaller grain size possesses a lower discharge energy density but higher energy storage efficiency.
- ➤ When the applied electric field reaches their own breakdown strength, the smallest grain-sized ceramic displays the largest discharge energy density and energy storage efficiency.
- ➤ It is highly suggested that ferroelectric ceramics with smaller grain sizes can be used for applications in energy storage devices.



Thanks!